

Christopher W. Fletcher

CONTACT INFORMATION	1819 Berkeley Way Berkeley CA, 94703	Phone: (310) 869-0400 E-mail: cwfletcher@berkeley.edu	Website: http://cwfletcher.net Last updated: August, 2009
OBJECTIVE	Seeking a team-oriented VLSI design or software engineering position to solve challenging problems in emerging fields.		
EDUCATION	The University of California, Berkeley	August, 2006 - Present	
	B.S. candidate, Electrical Engineering and Computer Science 4 th Year, Senior Standing (Graduation: May 2010, expected) Cumulative GPA: 3.91 / 4.0		
SKILLS	Languages: Java, C, Scheme, TCL, MIPS & SPARC Assembly, Ruby, Verilog, basic Unix shell scripts, TeX Technologies: SOAP, REST, JAXP/JAXB, XML Schema, Apache ANT, Java Swing, SVN Applications: Eclipse, ModelSim, Xilinx ISE (Project IDE, ChipScope, FPGA Editor), Synplify Pro, Gimp, Windows office Operating Systems: Solaris, Linux (Ubuntu), Windows		
RESEARCH EXPERIENCE	RAMP (UC Berkeley)	Jan 2008 - Present	
	<ul style="list-style-type: none">• Position: Student researcher for the “Research Accelerator for Multiple Processors” group• Designed and implemented a “Credit-Based Flow Control” FIFO link for the RAMP host system• Architected, implemented and maintain a latency-insensitive SPARCv8 processor (support for all integer unit instructions and traps) optimized for Xilinx Virtex5 FPGAs		
	Systems Biology (UC Berkeley, Stanford)	May 2009 - Present	
	<ul style="list-style-type: none">• Implemented a scalable communications solution between a software headnode and FPGA• Optimized a hardware implementation of an MCMC algorithm kernel for a Xilinx Virtex5 FPGA• Architected a multi-FPGA solution of the above design to a rack of FPGA boards		
EMPLOYMENT	Oracle, Software Engineering Summer Intern	May - Aug 2008	
	<ul style="list-style-type: none">• Designed and coded a connector between Oracle’s JDeveloper and Atlassian’s JIRA issue tracker		
	Head Teaching Assistant (UC Berkeley)	Aug 2008 - Jun 2009	
	<ul style="list-style-type: none">• Course: Components and Design Techniques for Digital Systems (CS150)• Average Rating: 4.8/5.0• Held weekly office hours, discussion sections, and lab sections• Presented class-wide “lab lectures” and prepared lab lecture material• Rewrote lab documents and supplementary code/solutions• Wrote additional tutorials for Verilog HDL, SVN, CAD tools, and hardware interfaces• Maintained class website		
	Other Undergraduate Computer Science Course Positions		
	• Served as lab assistant for a UC Berkeley computer science course	Jan - May 2007	
	• Graded student assignments for a UC Berkeley computer science course	Aug - Dec 2007	
MISC. PROJECTS	<ul style="list-style-type: none">• Coded and maintain a library of Apache ANT tasks for controlling Electronic Design Automation tools Summer 2008• Implemented a “Wireless Video Conferencing System with Video Compression” on a Xilinx VirtexE		

FPGA **Jan - May 2008**
 • Designed and coded “Tutorade,” an application designed to help disadvantaged children with math and grammar **May - Aug 2007**

ACTIVITIES

The Engineers’ Joint Council **2006 - 2008**

- **Position:** Corporate Liaison Committee Chair (Officer)
- Recruited and directed a group of 22 committee members in gathering corporate sponsors for on-campus events
- Published action plans to be executed by committee members
- Organized and facilitated committee meetings

The Zeitgeist Community Learning Center **2005 - 2007**

- Established and directed a Computer Literacy Program for at risk children from Los Angeles’ Crenshaw District
- Tutored students in various academic subjects
- Prototyped, tested, and distributed “Tutorade” amongst the students

The ALS Association - Greater Los Angeles Chapter **2005 - 2006**

- Coordinated all school activities with the ALS Association, Greater Los Angeles Chapter
- Designed and executed the first “Viewpoint School ALS Walk”
- Raised \$14,477.00 for the ALS (Lou Gehrig’s Disease) Association

RELEVANT
COURSEWORK
(EECS DEP.)

VLSI Systems Design	2009
Programming Languages and Compilers	2009
Hardware Design Patterns	2009
Efficient Algorithms and Intractable Problems	2009
Operating Systems and System Programming	2009
Computer Architecture and Engineering	2009
“Capstone” Digital Design Laboratory	2008
Signals and Systems	2008
Introduction to Microelectronic Circuits	2008
Components and Design Techniques for Digital Systems	2008
Discrete Math and Probability	2008
Machine Structures	2007
Data Structures and Programming Methodology	2007
Web 2.0 Programming Using Ruby on Rails	2007
The Structure and Interpretation of Computer Programs	2006

HONORS, AWARDS
AND AFFILIATIONS

EECS Honors Degree (Breadth focus: Economics), candidate	2009
Golden Key, Honor Society, member	2008
Tau Beta Pi, Honor Society, member	2007
Eta Kappa Nu, Honor Society, invited	2007
Rose Hills Scholar, Academic Distinction	2007-2008
Kraft Scholar, Academic Distinction	2006
National Society of Collegiate Scholars, Honor Society, member	2006
VSSA Award, Community Service Distinction	2006
Cum Laude Society, Honor Society, member	2006
CORE, Community Service Honor Society, member	2005

REFERENCES

Available upon request